Lab 5 & 6 simulations

1. 8 line to 3 line Priority Encoder with active low input lines and active high output lines. Priority sequence is given as I2>I4>I0>I7>I5>I6>I1>I3

Graphical user interface, text, application

Description automatically generated

Table

Description automatically generated

1. 4 line to 16  
   line Binary Decoder with active low enable input, active low output lines and active high input lines.

Text, table

Description automatically generated

A picture containing table

Description automatically generated

1. 12 line to 1  
   line Multiplexer with active low enable input, active high input and  
   output lines.

Graphical user interface, text

Description automatically generated

Graphical user interface, application, table

Description automatically generated

Graphical user interface, text

Description automatically generated

Graphical user interface, application, table, Excel

Description automatically generated